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For Immediate Release

Cypress Samples 9 Mbit QDR™ Memories

QDR SRAMs Provide Higher Bandwidth to Networking and Telecom Applications In High-Growth Communications Markets Than Competing Memory Standards

SAN JOSE, Calif., March 30, 2001 – Cypress Semiconductor (NYSE: CY) today announced the availability of samples for its 9 Mbit Quad Data Rate™ (QDR™) SRAMs, which have the capability to deliver data as fast as 333 MHz. These synchronous SRAM devices enable higher bandwidth in networking and telecommunications applications, supporting Cypress's focus on high-growth market segments – wide-area networks (WAN), storage array networks (SAN), wireless infrastructure (WIN), and wireless terminals (WIT).

“QDR is being adopted quickly as the next-generation SRAM solution for OC-48 and OC-192 applications. Its functionality simplifies system design, while providing the highest bandwidth of any current SRAM solution. As a result, we have won over a number of designs at our top-tier networking customers,” said Antonio Alvarez, vice president of the Memory Products Group at Cypress. “The adoption of QDR is a genuine success story in the SRAM industry.”

The Cypress QDR memories increase bandwidth by supporting separate data inputs and outputs for read and write operations. Independent ports transferring data on both edges of the clock result in a quad-speed transfer of data. The result is maximum bandwidth and the elimination of the need to “turn around” the data bus, which is required when using common I/O devices.

The new devices offer a maximum data transfer speed of 333 MHz and are configured as 512K x 18-bit memories. They are packaged in a 165-ball fine-pitch ball grid array (fBGAs) package that is 37 percent smaller than the fBGA packages currently being used for SRAMs.

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About QDR

QDR SRAMs target the next generation of switches and routers that operate at data rates beyond 200 MHz. To ensure that customers have multiple, compatible sources, the QDR co-development team – comprised of Cypress, IDT, Micron, and NEC – has defined roadmaps and migration paths. The co-development team has partnered beyond simple data sheet compliance by exchanging design simulations, test vectors, test methodologies, characterization plans, and common packaging support.

The development of the unique QDR SRAM architecture combined extensive input from networking industry leaders. The devices are designed to offer greatly increased memory bandwidth over current SRAM solutions. Each co-development partner designs and manufactures its QDR devices in its own technology and fabrication facilities, and will deliver products according to its own schedule. More information about the technology is available at www.qdrsram.com.

Price and Availability

The Cypress 9 Mbit QDR SRAMs will be priced at \$44.95 in volumes of 1,000. Samples of the CY7C1302V25 (2-word burst version) are available now, with production volumes expected at the end of May. Samples of the CY7C1304V25 (4-word burst version) will be available in mid-May, with production volumes expected in mid-June.

About Cypress

Cypress Semiconductor is “Driving the Communications Revolution”™ by providing high-performance integrated circuit solutions to fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial control. With a focus on emerging communications applications, Cypress's product portfolios include high-speed data communications ICs; networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices; timing technology solutions; and controllers for Universal Serial Bus (USB).

More than two-thirds of Cypress's sales come from fast-growing communications markets and dynamic companies such as Alcatel, Cisco, Ericsson, Lucent, Motorola, Nortel Networks, and 3Com. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

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Cypress employs more than 4,500 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide Web site at <http://www.cypress.com>.

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“Driving the Communications Revolution” is a trademark of Cypress Semiconductor. Quad Data Rate and QDR are trademarks of the QDR co-development team.